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(Use as many sheets as necessary)

Sheet 1 of 1

Complete if Known	
Application Number	10/761,048
Filing Date	01/20/2004
First Named Inventor	Stephen R. Van Doren
Art Unit	2446
Examiner Name	Ali, Farhad
Attorney Docket Number	200313612-1

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T[2]
		MARTIN, MILO M.K., ET AL., "Token Coherence: Decoupling Performance And Correctness", ISCH-30, pages 1-12, June 9-11, 2003	
		RAJEEV, JOSHI ET AL., " Checking Cache-Coherence Protocols With TLA+", Kluwer Adacemic Publishers, 2003, pages 1-8	
		HANDY, "The Cache Memory Book", 1998, Academic Press second edition, pages 144-155	
		CHEN ET AL., "Write Caching in Distributed Filed Systems", Distributed Computing Systems, 1995., Proceedings Of The 15th Int'l Con., 30 May-2 June 1995, Pages 457-466	
		ARCHIBALD ET AL., "Cache Coherence Portocols: Evaluation Using A Mulitprocessor Simulation Model", November 1986, ACM Transactions on Computer Systems, Pages 273-298	
		GHARACHORLOO, KOUROSH ET AL., "Architecture and Design of AlphaServer GS320", Western Research Laboratory, (Date Unknown)	
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		MARTIN ET AL., "Bandwidth Adaptive Snooping", Proceedings Of The Eighth International Symposium On High-Performance Computer Architecture (HPCA'02), 2002 IEEE	
		ACACIO, MANUEL ET AL., "Owner Prediction For Accelerating Cache-to-Cache Transfer Misses In A cc-NUMA Architecture", IEEE 2002	

Examiner Signature		Date Considered	

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1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.

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